



SDM05U40CSP

### 0.5A SCHOTTKY BARRIER RECTIFIER CHIP SCALE PACKAGE

## **Product Summary**

V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	V <sub>F MAX</sub> (V)	I <sub>R MAX</sub> (μA)
40	0.5	0.46	75

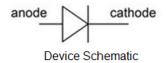
## **Description**

The SDM05U40CSP is a 40-volt 0.5A Schottky barrier rectifier that is optimized for low forward voltage drop and low leakage current, housed in a compact chip scale package (CSP) that occupies only 0.6mm² board-space. The low thermal resistance enables designers to meet design challenges of increasing efficiency whilst at the same time reducing board space.

## **Applications**

It is ideally suited for use in portable applications as a:

- Blocking Diode
- Boost Diode
- Switching Diode
- Reverse Protection Diode



### **Features and Benefits**

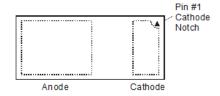
- Off Board Profile of 0.275mm More than 30% Thinner than DFN1006
- Low Forward Voltage (V<sub>F</sub>) Minimizes Conduction Losses and Improves Efficiency
- Reduced High Temperature Reverse Leakage; Increased Reliability Against Thermal Runaway Failure in High Temperature Operation
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

## **Mechanical Data**

Case: X3-WLB1006-2

Moisture Sensitivity: Level 1 per J-STD-020

Polarity: Cathode DotWeight: 0.001 grams



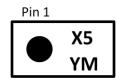
## **Ordering Information** (Note 4)

Part Number	Case	Packaging
SDM05U40CSP-7	X3-WLB1006-2	5,000/ Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/quality/lead\_free.htmlfor more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

# **Marking Information**



X5= Product Type Marking Code YM=Date Code Marking Y= Year (ex: B= 2014) M=Month (ex: 9= September) Dot Denotes Cathode Pin

Date Code Key

Year	2014	4	2015		2016	20	17	2018		2019	2	2020
Code	В		С		D		Ε	F		G		Н
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D



# **Maximum Ratings** (@ $T_A = +25^{\circ}C$ , unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$	40	V
Average Rectified Output Current	lo	0.5	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	IFSM	14	А

# Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	$R_{ heta JA}$	135	°C/W
Typical Thermal Resistance Junction to Ambient (Note 6)	$R_{ heta \mathrm{JA}}$	80	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

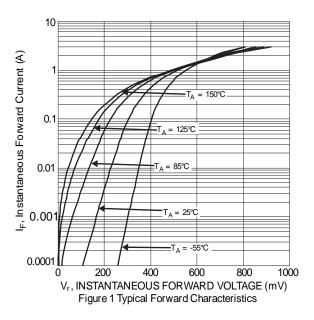
# **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

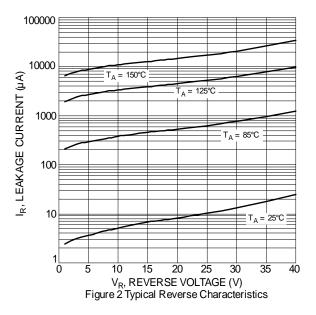
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Forward Voltage Drop		-	0.305	0.36	mV	I <sub>F</sub> = 0.1A
	V <sub>F</sub>	-	0.415	0.46		$I_F = 0.5A$
		-	0.34	-		$I_F = 0.5A, T_J = +125^{\circ}C$
Leakage Current (Note 7)	I <sub>R</sub>	-	-	15	μΑ	V <sub>R</sub> = 10V
		-	-	75		V <sub>R</sub> = 40V
Junction Capacitance	Ст	-	35	-	pF	V <sub>R</sub> = 4V, f = 1.0MHz

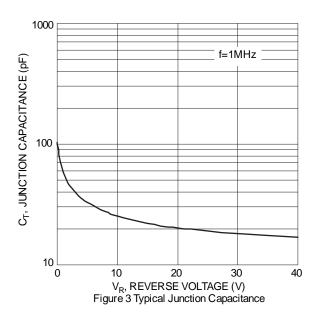
Notes:

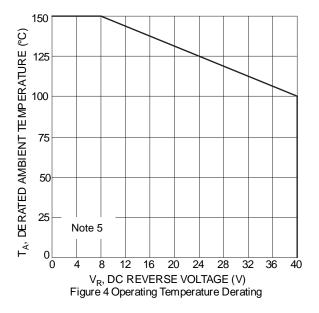
- Device mounted on FR-4 PCB, 2oz. Copper, minimum recommended pad layout per http://www.diodes.com/datasheets/ap02001.pdf.
  Device mounted on FR-4 PCB, 2oz. 1 square inch Copper.
  Short duration pulse test used to minimize self-heating effect.







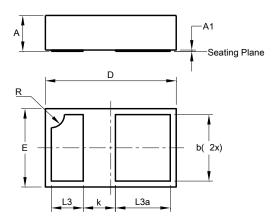






# **Package Outline Dimensions**

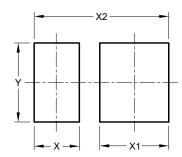
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	X3-WLB1006-2						
Dim	Min	Max	Тур				
Α	0.25	0.30	0.275				
<b>A</b> 1	0.00	0.01	-				
b	0.450	0.550	0.500				
D	0.95	1.05	1.000				
Е	0.55	0.65	0.600				
k	-	-	0.288				
L3	0.194	0.294	0.244				
L3a	0.350	0.450	0.400				
R	-	-	0.100				
All	All Dimensions in mm						

# **Suggested Pad Layout**

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value
	(in mm)
Х	0.332
X1	0.507
X2	0.989
Υ	0.579



#### IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

### LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
  - 1. are intended to implant into the body, or
  - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2014, Diodes Incorporated

www.diodes.com